APPLICATION NO.

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PATENT NUMBER

O.I.P.E.

CLASS

PATENT DATE

SUBCLASS ART UNIT, 257 2815 Brock 554

William Stearns Nozar Hassanzadeh

CONT/PRIOR

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Optimized circuit design layout for high performance ball grid array packages

PTO-2040 12/99

ISSUING CLASSIFICATION									
ORIGINAL		CROSS REFERENCE(S)							
CLASS SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)						
INTERNATIONAL	LCLASSIFICATION								
<del>                                      </del>			Continued on Issue Slip Inside File Jacket						

TERMINAL	DRAWINGS			CLAIMS ALLOWED	
DISCLAIMER	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
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subsequent to (date) has been disclaimed.	(Assistant Examiner) (Date)				
The term of this patent shall not extend beyond the expiration date of U.S Patent. No.				ISSI	UE FEE
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this patent have been disclaimed.	(Legal Instruments Examiner) (Date)			l	
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